

AMENDMENTS

In the Claims:

This listing of claims replaces all prior versions, and listings, of claims in the application.

1. (Currently Amended) An electroluminescent display device, comprising:
a plurality of pixels;
an electroluminescent element disposed in each of the pixels;
a pixel selecting thin film transistor disposed in each of the pixels and selecting the corresponding pixel in response to a gate signal; and
a driving thin film transistor disposed in each of the pixels comprising a plurality of transistors connected in series and supplying an electric current to the corresponding electroluminescent element in response to a display signal supplied through the corresponding pixel selecting thin film transistor, the driving thin film transistor comprising a plurality of gates.
2. (Original) The electroluminescent display device of claim 1, wherein the pixel selecting thin film transistor is configured to have only one gate.
3. (Original) The electroluminescent display device of claim 1, wherein a total number of gates of the pixel selecting thin film transistor is smaller than a total number of the gates of the driving thin film transistor.
4. (Canceled)
5. (Original) An electroluminescent display device, comprising:
a plurality of pixels;
an electroluminescent element disposed in each of the pixels;
a pixel selecting thin film transistor disposed in each of the pixels and selecting the corresponding pixel in response to a gate signal; and
a driving thin film transistor comprising a set of transistors connected in series, the driving thin film transistor being disposed in each of the pixels and supplying an electric current

to the corresponding electroluminescent element in response to a display signal supplied through the corresponding pixel selecting thin film transistor.

6. (Original) The electroluminescent display device of claim 5, wherein the driving thin film transistor further comprises an additional set of transistors connected in series, and the two sets of the transistors connected in series are connected in parallel.